

Giuseppe Iannaccone

Giuseppe Iannaccone is Professor of electronics at the University of Pisa, Italy. His interests include the fundamentals of transport and noise in nanoelectronic and mesoscopic devices, the development of device modeling and TCAD tools, and the design of extremely low-power circuits and systems for RFID and ambient intelligence scenarios. He has published more than 150 papers in peer-reviewed journals and more than 110 papers in proceedings of international conferences, receiving more than 1700 citations indexed by ISI-WoS.

Giuseppe Iannaccone has coordinated a few European and National Projects involving multiple partners and has acted as the Principal Investigator in several research projects funded by public agencies at the European and National levels and by private organizations.

He acts as a reviewer for a few funding agencies in Europe and is or has been in the technical committee of several international conferences in the field of semiconductor technology and design. Prior to joining the University of Pisa in 1996, Iannaccone was a researcher with the Italian National Research Council. He received the M.S. and Ph.D. degrees in electrical engineering from the University of Pisa in 1992 and 1996, respectively.